

POWERTIP CORPORATION

CD		$rac{1}{2}$	ın	NIC
35		CAT	w	ING

CUSTOMER		DTC	
COSTOWIER		PTC	
SAMPLE CODE (Ve	er.) :		
		This Code will be changed while	e mass production)
MASS PRODUCTION	CODE (Ver.) :	PG320240WRF-HNN	HS5 (Rev.0)
	_		
	Customer	Approved	
1		Date:	
		Date:	
Sales Sign	QC Confirmed	Date:	Designer
Sales Sign	QC Confirmed FEB 0 3. 205		Designer W//江 新聞体 E住子
Sales Sign Approval For Specific	FEB O 3. 305	Checked By	94//江 解發
Approval For Specification is	FEB 0 3. 305 ications Only.	Checked By 新養 34. 2. 16 圖香 新養 第3. 1. 31 東意 otice.	94//1 新發 第四年 94.1.26
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RECORDS OF REVISION

Date	Rev.	Description	Note	Page
2005/1/25	0	Mass Production		

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2. Package

Note: For detailed information please refer to IC data sheet: SITRONIX---ST8024-F4

SITRONIX---ST8016-F3

(Or comparable IC)



1. SPECIFICATIONS

1.1 Features

Item	Standard Value
Display Type	320 * 240 Dots
LCD Type	FSTN, Positive, Transflective
Driver Condition	LCD Module: 1/240 Duty, 1/14 Bias
Viewing Direction	6 O'clock
Backlight	LED B/L
Weight	90 g
Interface	8 bits parallel data input
Driver IC	Driver IC:ST8024-F4,ST8016-F3

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension	92.0 (L) * 71.3 (w) * 7.9(H)(Max)	mm
Viewing Area	78.78 (L) * 59.58 (w)	mm
Active Area	76.78 (L) * 57.58 (w)	mm
Dot Size	0.22 (L) * 0.22 (w)	mm
Dot Pitch	0.24 (L) * 0.24 (w)	mm

Note: For detailed information please refer to LCM drawing

1.3 Absolute Maximum Ratings

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply Voltage	V_{DD}	_	-0.3	+7.0	V
LCD Driver Supply Voltage	V_{EE}	_	-0.3	45	V
Input Voltage	V_{IN}	_	-0.3	V _{DD} +0.3	V
Operating Temperature	T _{OP}	_	-20	70	°C
Storage Temperature.	T _{ST}	_	-30	80	°C
Storage Humidity	H_D	Ta<40 ℃	20	90	%RH



1.4 DC Electrical Characteristics

 $V_{DD} = 5 \text{ V} \pm 5\%$, $V_{SS} = 0 \text{V}$, $Ta = 25^{\circ}\text{C}$

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Logic Supply Voltage	V _{DD}	_	4.75	5	5.25	V
"H" Input Voltage	V _{IH}	_	0.8 V _{DD}	-	-	V
"L" Input Voltage	V _{IL}	_	-	-	0.2 V _{DD}	V
"H" Output Voltage	V _{OH}	_	V _{DD} -0.4	-	-	V
"L" Output Voltage	V _{OL}	_	-	-	0.4	V
Supply current 1	I _{DD}	V _{OP} = 20.5V	-	0.2	0.6	mA
		-20°C	20.7	21.0	21.3	
	\/	0°C	20.2	20.5	20.8	
LCM driving voltage	Vop	25°C	20.2	20.5	20.8	V
	(Vc ₉)	50°C	19.8	20.1	20.4	
		70°C	18.8	19.1	19.4	

Test condition: M:35Hz FLM:70Hz

Note: Need to make sure that there is no flicker and ripper phenomenon when setting the

1.5 Optical Characteristics

frame frequency in your set .

LCD Panel: 1/240 Duty, 1/15 Bias, $V_{LCD} = 22.0V$, Ta = 25° C

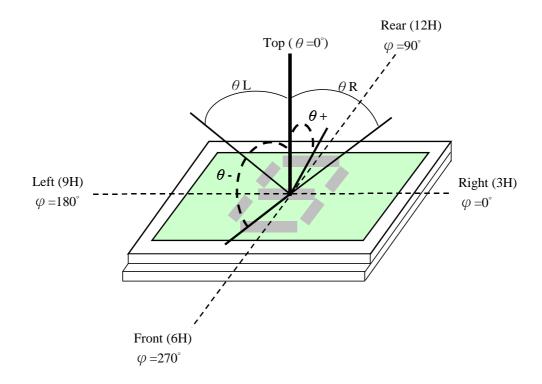
					· LCD ==	
Item	Symbol	Conditions	Min.	Тур.	Max.	Reference
View Angle	θ	C <u>></u> 2.0, ∅ = 270°	-40°	-	40°	Notes 1
Contrast Ratio	С	θ =-5°, Ø = 270°	2	3	-	Note 3
Response Time(rise)	tr	θ =-5°, Ø = 270°	-	170 ms	255 ms	Note 2
Response Time(fall)	tf	θ =-5°, Ø = 270°	-	350 ms	525 ms	Note 2



Note 1.

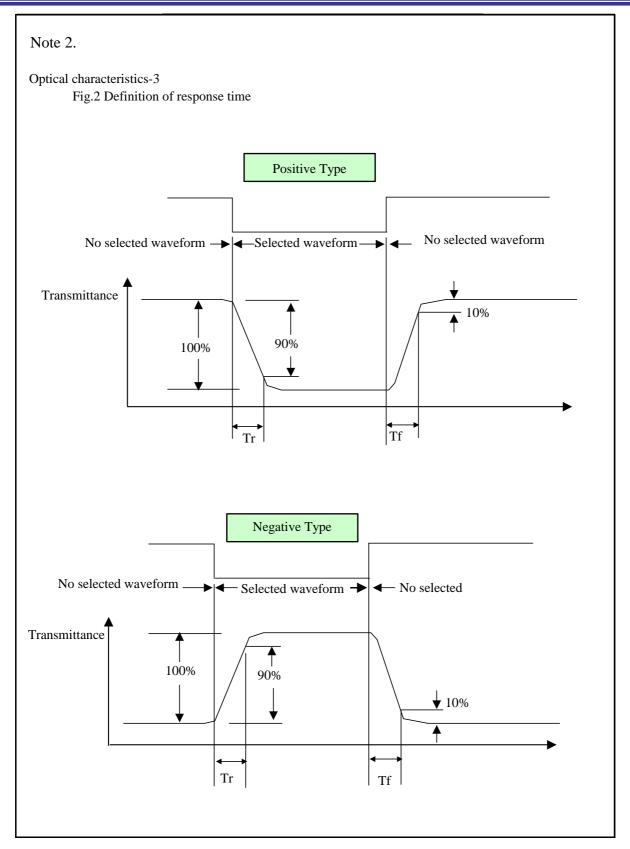
Optical characteristics-2

Viewing angle



Viewing angle





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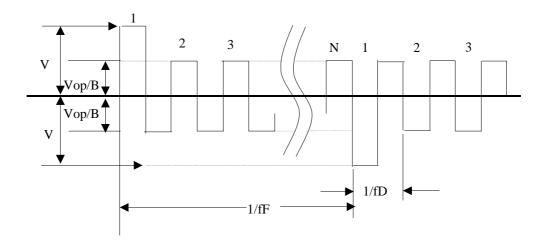
Electrical characteristics-2

2 Drive waveform

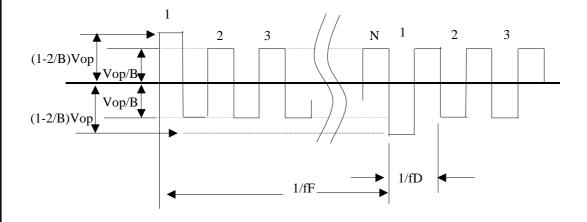
Vop: Drive voltage fF: Frame frequency 1/B: Bias fD: Drive frequency

N: Duty

(1) Selected waveform



(2) Non- Selected waveform

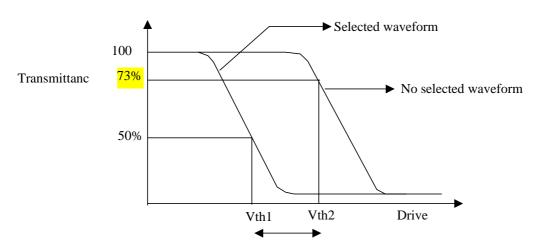


Note:

Frame frequency is defined as follows: Common side supply voltage peak - to - peak /2=1 period



Note 3.: Definition of Vth



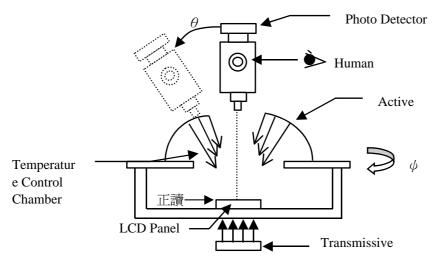
Active voltage range

	Vth1	Vth2
View direction	10°	40°
Drive waveform	(Selected waveform)	(No selected waveform)
Transmittance	50%	73%

※1 Contrast ratio

= (Brightness in OFF state) / (Brightness in ON state)

Outline of Electro-Optical Characteristics Measuring System





1.6 Backlight Characteristics

LCD Module with LED Backlight

Maximum Ratings

Item	Symbol	Conditions	Min.	Max.	Unit
Forward Current	IF	Ta =25°C	-	120	mA
Reverse Voltage	VR	Ta =25°C	-	5	V
Power Dissipation	РО	Ta =25°C	-	0.51	W

Electrical / Optical Characteristics

Ta =25°C

					iu	23 0
Item	Symbol	Conditions	Min.	Тур.	Max.	Unit
Forward Voltage	VF	IF= 120 mA	-	3.7	4.2	V
Reverse Current	IR	VR= 5 V	-	-	10	uA
Average Brightness	IV	IF= 120 mA	15.0	30.0		cd/m ²
CIE Color Coordinate	X	IF= 120 mA	0.25	0.31	0.37	
(With LCD)	Y	IF- IZU IIIA	0.28	0.34	0.40	-
Uniformity	ΔΒ	IVMin / IVMax *100%	70	-	-	%
Color			White			

^{*1} This value will be changed while mass production.

^{*2 △}B= (min / max) %



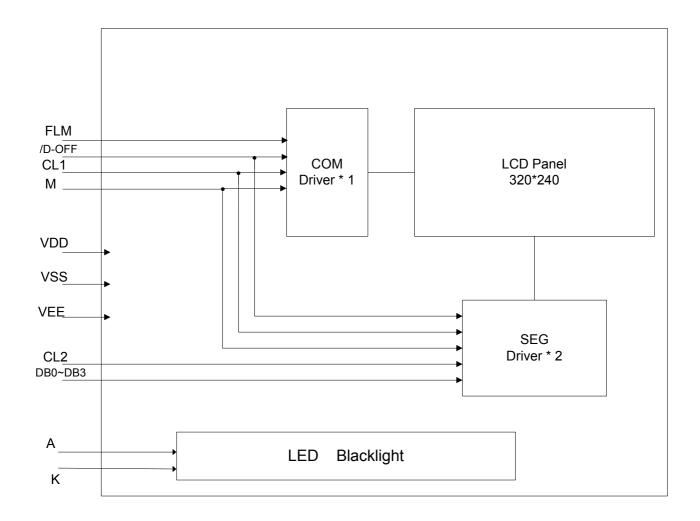
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

* See Appendix

2.1.2 Block Diagram





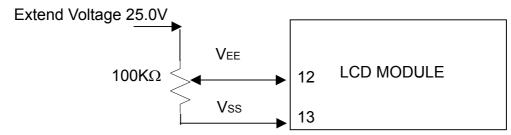
2.2 Interface Pin Description

PIN	Symbol	DESCRIPTION
1	FLM	Indicates the beginning of each display cycle.
2	M	AC signal input for LC driving waveform
3	CL1	Shift clock pulse input pin.
4	CL2	Clock input pin for taking display data
5	/D-OFF	Control input pin for output deselect level, active "L"
6	DB0	Display data input pin
7	DB1	Display data input pin
8	DB2	Display data input pin
9	DB3	Display data input pin
10	V_{DD}	Logic system power supply pin
11	Vss	Ground pin
12	V _{EE}	LCD Operator Voltage
13	Vss	Ground pin
14	NC	Must be open.
15	А	Power supply for LED B/L. (Anode)
16	K	Power supply for LED B/L. (Cathode)

Note: FLM Recommended 55Hz ~ 75Hz

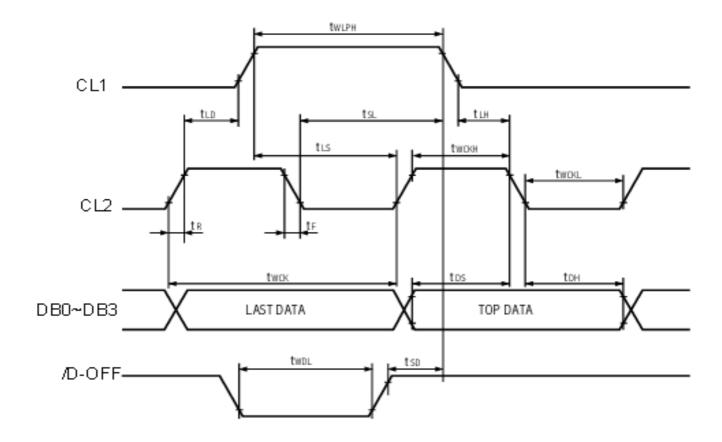
M Recommended 200Hz ~ 400Hz

• Contrast Adjust



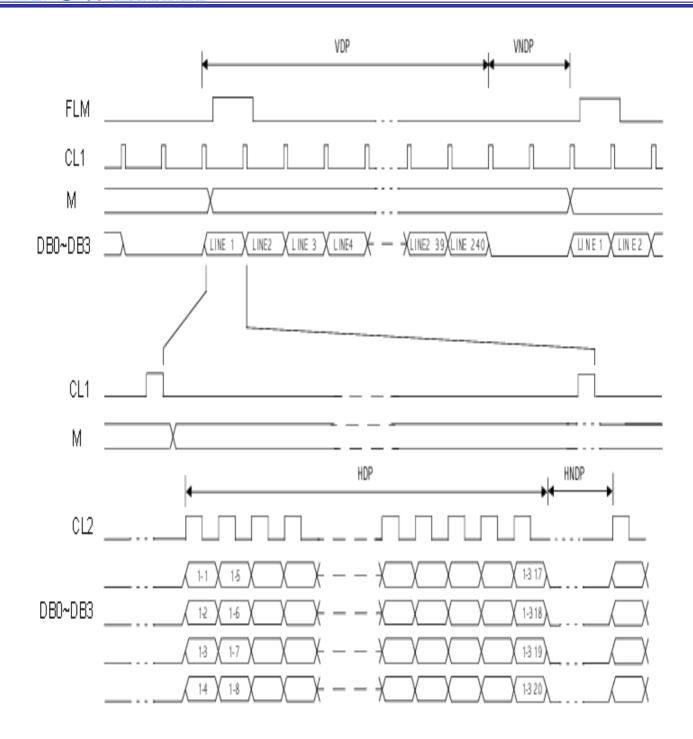


2.3 Timing Characteristics



Parameter	Symol	Condition	Min.	Тур.	Max.	Unit
Shift clock period	t wck	t _r ,t _f ≦11 ns	125			ns
Shift clock "H" pulse width	t wckh		51			ns
Shift clock "L" pulse width	t wckl		51			ns
Data setup time	t _{DS}		30			ns
Data hold time	t _{DH}		40			ns
Latch pulse "H" pulse width	t _{WLPH}		51			ns
Shift clock rise to Latch pulse rise time	t LD		0			ns
Shift clock fall to Latch pulse fall time	t _{SL}		51			ns
Latch pulse rise to Shift clock rise time	t LS		51			ns
Latch pulse fall to Shift clock fall time	t _{LH}		51			ns
Input signal rise time	t r				50	ns
Input signal fall time	t _f				50	ns
/D-off removal time	t _{SD}		100			ns
/D-off "L" pulse width	t _{WDL}		1.2			us
Output delay time (1)	t _{pd1} ,t _{pd2}	CL=15 pF			1.2	us
Output delay time (2)	t _{pd3}	CL=15 pF			1.2	us

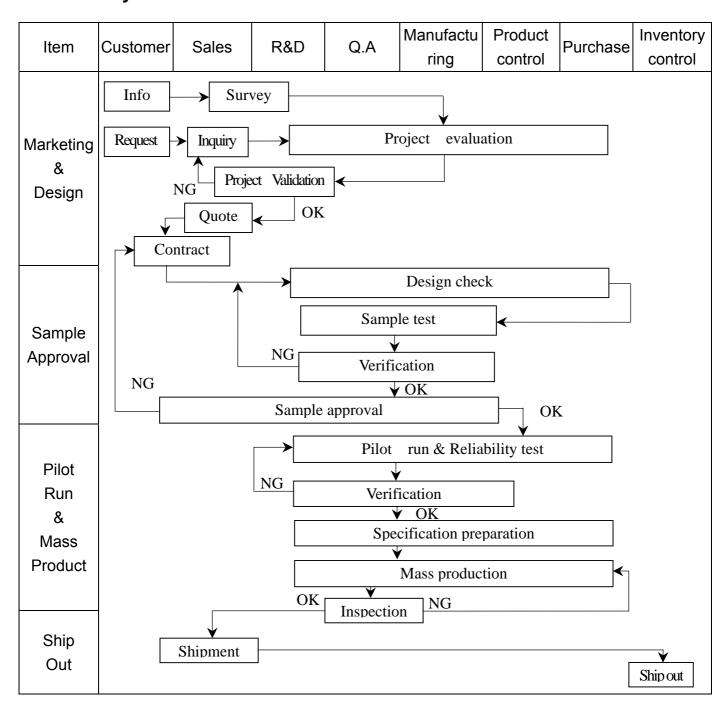




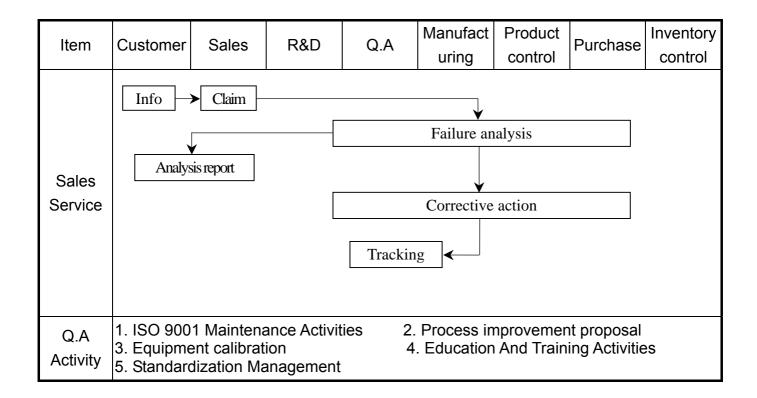


3. QUALITY ASSURANCE SYSTEM

3.1 Quality Assurance Flow Chart









3.2 Inspection Specification

Equipment : Gauge \ MIL-STD \ Powertip Tester \ Sample \ IQC Defect Level : Major Defect AQL 0.4; Minor Defect AQL 1.5 \ \cdots

FQC Defect Level: 100% Inspection • OUT Going Defect Level: Sampling •

Specification:

NO	Item	Specification	Judge	Level
1	Part Number	The part number is inconsistent with work order of production	N.G.	Major
2	Quantity The quantity is inconsistent with work order of production			Major
	Electronic characteristics of LCM	The display lacks of some patterns.	N.G.	Major
		Missing line.	N.G.	Major
3		The size of missing dot, A is > 1/2 Dot size	N.G.	Major
	A=(L + W)÷2	There is no function.	N.G.	Major
	7. (=) =	Output data is error	N.G.	Major
		Material is different with work order of production	N.G.	Major
		LCD is assembled in inverse direction	N.G.	Major
		Bezel is assembled in inverse direction	N.G.	Major
	_	Shadow is within LCD viewing area + 0.5 mm	N.G.	Major
	Appearance of	The diameter of dirty particle, A is > 0.4 mm	N.G.	Minor
	LCD A=(L + W)÷2	Dirty particle length is $>$ 3.0mm, and 0.01mm $<$ width \leq 0.05mm	N.G.	Minor
4	Dirty particle (Including scratch \ bubble) Appearance of PCB A=(L+W)÷2	Display is without protective film	N.G.	Minor
		Conductive rubber is over bezel 1mm	N.G.	Minor
		Polarizer exceeds over viewing area of LCD	N.G.	Minor
		Area of bubble in polarizer, A > 1.0mm, the number of bubble is > 1 piece.	N.G.	Minor
		0.4mm < Area of bubble in polarizer, A < 1.0mm, the number of bubble is >4 pieces.	N.G.	Minor
		Burned area or wrong part number is on PCB	N.G.	Major
		The symbol, character, and mark of PCB are unidentifiable.	N.G	Minor
		The stripped solder mask , A is > 1.0mm	N.G.	Minor
5		0.3mm < stripped solder mask or visible circuit, A $<$ 1.0mm, and the number is \ge 4 pieces	N.G.	Minor
		There is particle between the circuits in solder mask	N.G	Minor
		The circuit is peeled off or cracked	N.G	Minor
		There is any circuits risen or exposed.	N.G	Minor
		0.2mm < Area of solder ball, A is \leq 0.4mm The number of solder ball is \geq 3 pieces	N.G	Minor
		The magnitude of solder ball, A is $>$ 0.4mm.	N.G	Minor

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NO	Item	Specification	Judge	Level
6		The shape of modeling is deformed by touching.	N.G.	Major
	Appearance of	Insufficient epoxy: Circuit or pad of IC is visible	N.G.	Minor
	molding A=(L + W)÷2	Excessive epoxy: Diameter of modeling is >20mm or height is >2.5mm	N.G.	Minor
		The diameter of pinhole in modeling, A is >0.2mm.	N.G.	Minor
		The folding angle of frame must be $>45^{\circ}$ +10°	N.G.	Minor
7	Appearance of frame	The area of stripped electroplate in top-view of frame, A is >1.0mm.	N.G.	Minor
'	A=(L + W)÷2	Rust or crack is (Top view only)	N.G.	Minor
	A-(L ' W).Z	The scratched width of frame is $>$ 0.06mm. (Top view only)	N.G.	Minor
	□ otricol	The color of backlight is nonconforming	N.G.	Major
	Electrical characteristic of	Backlight can't work normally.	N.G.	Major
8	backlight	The LED lamp can't work normally	N.G.	Major
	baokiigiit	The unsoldering area of pin for backlight,	N.G.	Minor
	A=(L+W)÷2 Assembly parts A=(L+W)÷2	A is >1/2 solder joint area.	NI O	B 4"
		The height of solder pin for backlight is >2.0mm	N.G.	Minor
		The mark or polarity of component is unidentifiable. The height between bottom of component and surface of the PCB is floating > 0.7mm	N.G.	Minor Minor
10		D>1/4W W D D D D' Pad	N.G.	Minor
		End solder joint width, D' is >50% width of component termination or width of pad	N.G.	Minor
		Side overhang, D is $>$ 25% width of component termination.	N.G.	Minor
		Component is cracked, deformed, and burned, etc.	N.G.	Minor
		The polarity of component is placed in inverse direction.	N.G.	Minor
		Maximum fillet height of solder extends onto the component body or minimum fillet height is <0.5mm.	N.G.	Minor



4. RELIABILITY TEST

4.1 Reliability Test Condition

NO	Item	Test Condition			
1	High Temperature Storage	Storage at 80 $\pm 2^{\circ}$ C 96~100 hrs Surrounding temperature, then storage at normal condition 4hrs			
2	Low Temperature Storage	Storage at -30 ±2°C 96~100 hrs Surrounding temperature, then storage at normal condition 4hrs			
3	High Temperature /Humidity Storage	 1.Storage 96~100 hrs 60±2°C, 90~95%RH surrounding temperature, then storage at normal condition 4hrs. (Excluding the polarizer). or 2.Storage 96~100 hrs 40±2°C, 90~95%RH surrounding temperature, then storage at normal condition 4 hrs. 			
4	Temperature Cycling	$-20^{\circ}\mathbb{C} \rightarrow 25^{\circ}\mathbb{C} \rightarrow 70^{\circ}\mathbb{C} \rightarrow 25^{\circ}\mathbb{C}$ (30mins) (5mins) (30mins) (5mins) 10 Cycle			
5	Vibration	10~55Hz(1 minute)1.5mm X,Y and Z direction * (each 2hrs)			
6	ESD Test	Air Discharge: Apply 6 KV with 5 times discharge for each polarity +/- Testing location: Around the face of LCD	Contact Discharge: Apply 250V with 5 times discharge for each polarity +/- Testing location: 1.Apply to bezel. 2.Apply to Vdd, Vss.		
7	Drop Test	Packing Weight (Kg) 0 ~ 45.4 45.4 ~ 90.8 90.8 ~ 454 Over 454	Drop Height (cm) 122 76 61 46		



5. PRECAUTION RELATING PRODUCT HANDLING

5.1 SAFETY

- 5.1.1 If the LCD panel breaks, be careful not to get the liquid crystal to touch your skin.
- 5.1.2 If the liquid crystal touches your skin or clothes, please wash it off immediately by using soap and water.

5.2 HANDLING

- 5.2.1 Avoid any strong mechanical shock which can break the glass.
- 5.2.2 Avoid static electricity which can damage the CMOS LSI—When working with the module , be sure to ground your body and any electrical equipment you may be using.
- 5.2.3 Do not remove the panel or frame from the module.
- 5.2.4 The polarizing plate of the display is very fragile. So , please handle it very carefully ,do not touch , push or rub the exposed polarizing with anything harder than an HB pencil lead (glass , tweezers , etc.)
- 5.2.5 Do not wipe the polarizing plate with a dry cloth, as it may easily scratch the surface of plate.
- 5.2.6 Do not touch the display area with bare hands, this will stain the display area.
- 5.2.7 Do not use ketonics solvent & aromatic solvent. Use with a soft cloth soaked with a cleaning naphtha solvent.

5.3 STORAGE

- 5.3.1 Store the panel or module in a dark place where the temperature is 25° C $\pm 5^{\circ}$ C and the humidity is below 65% RH.
- 5.3.2 Do not place the module near organics solvents or corrosive gases.
- 5.3.3 Do not crush, shake, or jolt the module.

5.4 TERMS OF WARRANTY

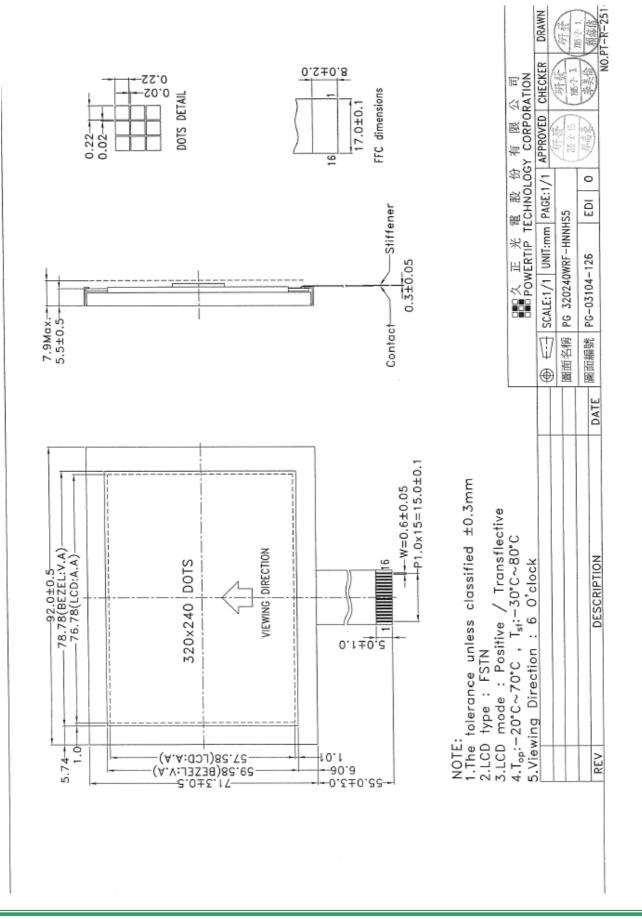
5.4.1 Applicable warrant period

The period is within thirteen months since the date of shipping out under normal using and storage conditions.

5.4.2 Unaccepted responsibility

This product has been manufactured to your company's specification as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we cannot take responsibility if the product is used in nuclear power control equipment, aerospace equipment, fire and security systems or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required.





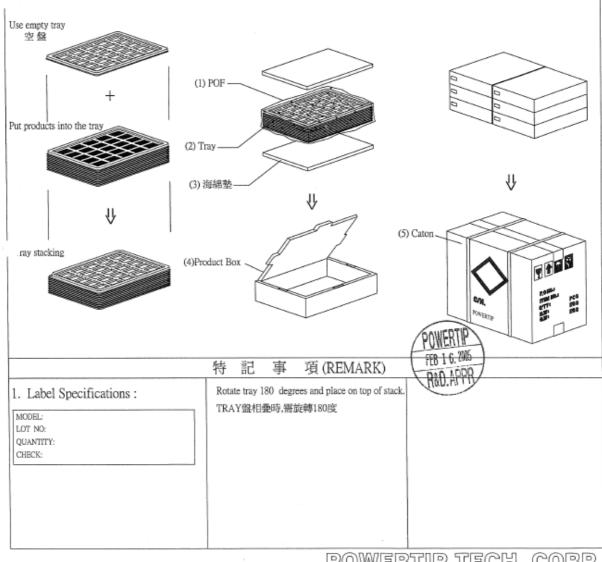


Customer	PTC	LCM包裝規格書	прриоте	38-1-26.	205-1-70.
100101-1-1	DC220040WDE UNNILIGE	LCM Packaging Specifications	DATE	建	版类Ver
LCM Model	PG320240WRF-HNNHS5	(For Tray)	05'01.26	05'01.26	01
- In the Light Let As a second of the second					

1.包裝材料規格表 (Packaging Material): (per carton)					
No.	Item	Model	Dimensions (mm)	Quantity	
1	成品 (LCM)	PG320240WRF-HNNHS5	92.0 X 71.3	96	
2	多層薄膜(1)POF	BA03	19"X350X0.015	6	
3	TRAY 盤 (2)	BX320240H6TZ0A	295 X 245 X 18.8	30	
4	海綿墊(3)	OTFOAM00006A0A	290 X 240 X 10	12	
5	C3内盒(4)Product Box	BX31025510AA0A	310 X 255 X 100	6	
6	外紙箱(5)Carton	BX52732536CC0A	527 X 325 X 360	1	
7					
8					
9					

2.單箱數量規格表 (Packaging Specifications and Quantity):

(1)LCM quantity per box : no per tray x no of tray 16 (2)Total LCM quantity in carton : quantity per box x no of boxes 16 6 96



POWERTIP TECH. CORP.